

# Process Model / Process Isolation

Isolation:

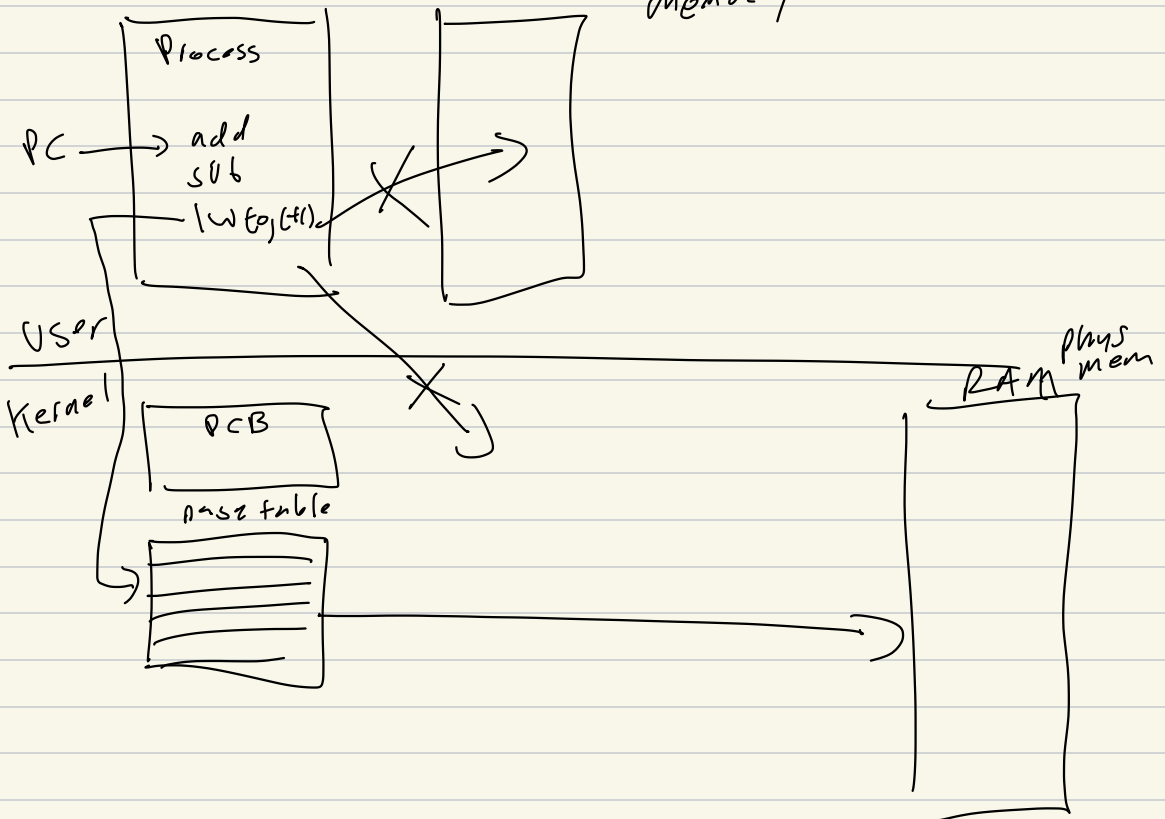
Processor Modes: Kernel / User

Interrupts: timer interrupt

Virtual Memory: page tables

Multiplex memory

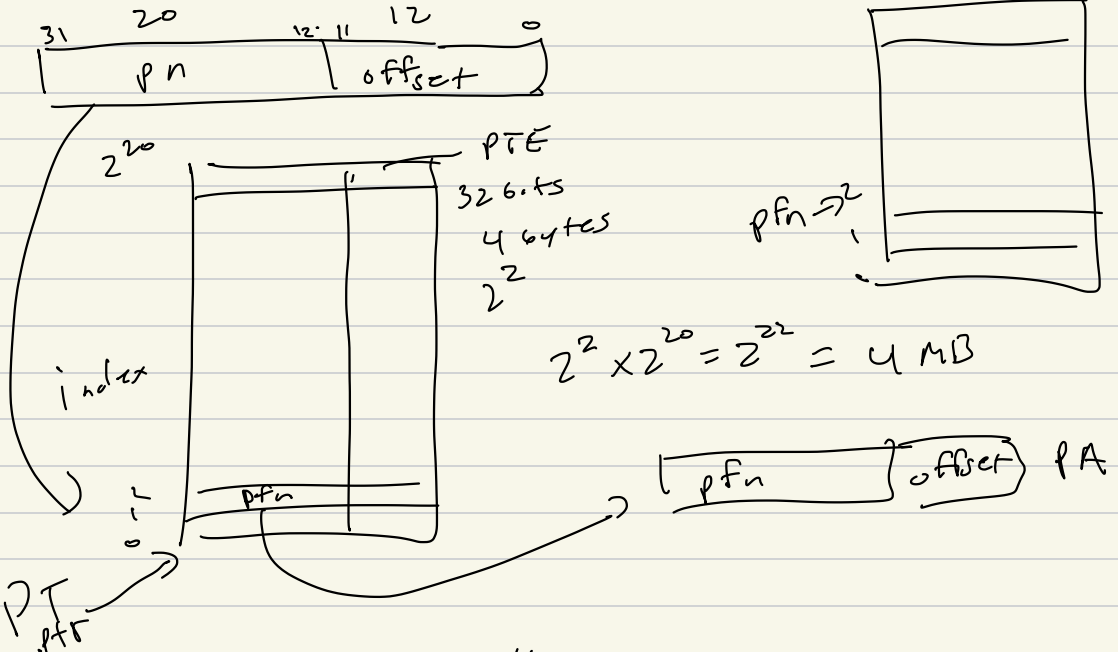
Multiplex CPU



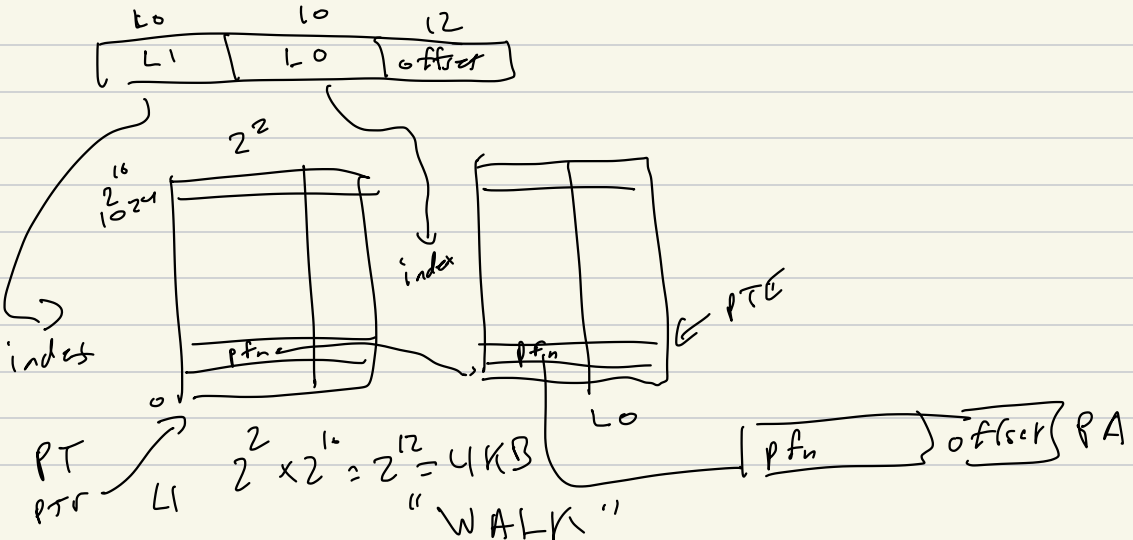
# Page Tables

32 bit example  $2^{32} = 4GB$

4KB page  
rdm



## Multi-level Page Tables

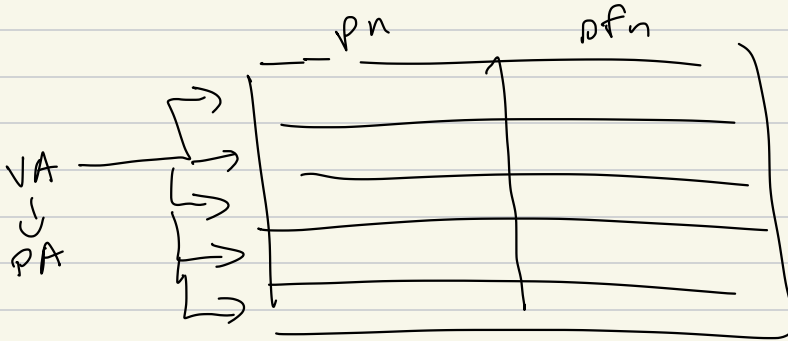


# Processor Translation

Translation Cache

TLB

Translation Lockside Buffer



99%  
hit rate

fully associative

# Room

